

Balanced-to-Unbalanced Bagley Power Divider with Input-Reflectionless Filtering Characteristics

Qi Chen, Zhongbao Wang^{*}, Shipeng Zhao, Hongmei Liu, and Shaojun Fang

Abstract—A novel balanced-to-unbalanced (BTU) Bagley power divider (BPD) with input-reflectionless filtering characteristics is proposed. It features a balanced input port and three single-ended output ports, which is difficult to achieve by means of conventional BTU power dividers. The filtering characteristics are achieved by parallel coupled lines. To further improve the differential-mode filtering selectivity, stepped impedance resonators are applied to introduce two transmission zeros near the passband. The input-reflectionless characteristic in the bandstop region is achieved by loading absorptive branches. For verifying the proposed power divider topology, a prototype of microstrip BTU Bagley power divider operating at 1.0 GHz is designed and fabricated with 3-dB filtering bandwidth of 72%. Furthermore, 10-dB input-reflectionless bandwidth covers the full measurement frequency from 0 to 2.5 GHz. Good agreement between the simulation and measurement validated the proposed method.

1. INTRODUCTION

Power dividers (PDs) are very important basic microwave components in various wireless communication systems [1]. Bagley power divider (BPD) is one of the planar microwave dividers that have attracted much attention recently. The BPD has several advantages over the other types of power dividers. Specifically, BPDs do not use lumped elements and can be easily extended to any number of output ports [2]. The development of BPDs [3–6] has advanced significantly in recent years. However, all these developments are based on single-ended types of PDs. Due to their benefits of differential-mode (DM) signal transmission, excellent common-mode (CM) suppression, high immunity to environmental noise, and ease of connecting with other balanced circuits [7], balanced power dividers have been utilized extensively in systems [8–10]. Subsequently, with the rapid development of miniaturization and integration of high-speed electronic systems, different kinds of balanced-to-balanced (BTB) filtering power dividers (FPDs) have been extensively studied [11–13].

Compared to BTB PDs, balanced-to-unbalanced (BTU) PDs also have the capability to suppress the CM noise [14–20]. Furthermore, BTU PD has the advantage that it can be connected with single-ended and balanced circuits at the same time. In [14], a BTU Wilkinson power divider was proposed, which used short-terminated four-wire coupled lines and a T-shaped cross section. But the 3-dB bandwidth is limited to only about 25%. In [15], by using three cascaded coupled lines and an isolation resistor, a compact wideband BTU out-of-phase PD was proposed. Subsequently, a BTU microstrip power divider based on coupled lines, which integrates a filtering function, was proposed [16]. Moreover, several BTU FPDs have also been studied [17–20]. A new BTU power divider was proposed to achieve a high level of CM rejection [17]. The filtering response is produced by loading shunt coupled-line stubs with numerous transmission zeros. In [18], a multifunctional broadband BTU FPD was reported, using a half-wavelength ring resonator and a coupled line section to achieve filtering performance. A high-selectivity BTU FPD using a dual-mode ring resonator was proposed [19]. The transmission zeros

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^{*} Corresponding author: Zhongbao Wang (wangzb@dlmu.edu.cn).

The authors are with the School of Information Science and Technology, Dalian Maritime University, Dalian, Liaoning 116026, China.

near the passband can be easily changed by choosing different pairs of characteristic impedances of ring resonators, and additional transmission zeros can be introduced by the loaded stubs. In order to better meet the requirements of modern wireless communication systems for miniaturization of components, a miniaturized BTU FPD with a wide upper stopband was proposed [20].

However, all the aforementioned FPDs give rise to undesired RF power reflections in their stopbands that may deteriorate the active stages in the RF front ends. Instead of reflecting non-transmitted RF input signal energy back to the source, RF reflectionless or absorptive bandpass filters [21–23] absorb it in their stopbands. Therefore, reflectionless performance can be achieved by introducing the absorptive branch [24] and parallel LC resonators [25] at the input or output ports of the FPD. To the best of our knowledge, the BTU Bagley power divider with input-reflectionless filtering characteristics has not been reported before. Consequently, in order to solve the problems of differential input, filtering, and reflectionless of the above Bagley power divider, it is necessary to design a BTU BPD with input-reflectionless filtering characteristics.

In this paper, a BTU Bagley power divider with input-reflectionless filtering characteristics is proposed, which has the function of common-mode suppression (CMS) and more flexibility of connecting with single-ended or balanced circuits. The filtering characteristics are achieved by parallel coupled lines. To further improve the differential-mode filtering selectivity, stepped impedance resonators are applied to introduce two transmission zeros near the passband. The input-reflectionless characteristic in the bandstop region is achieved by loading absorptive branches. One microstrip prototype is fabricated and measured. The experimental results agree well with the theoretical and simulated ones.

2. THEORETICAL ANALYSIS AND DESIGN

The initial circuit of the proposed BTU Bagley power divider is shown in Fig. 1. The balanced input ports are defined by ports 1+ and 1–, and the unbalanced output ports are defined by port 2, port 3, and port 4 in Z_0 . The mixed-mode scattering matrix (\mathbf{S}_{mm}) can be extracted from the standard scattering matrix (\mathbf{S}_{std}) using the matrix conversion, and the mixed-mode scattering matrix (\mathbf{S}_{mm}) can

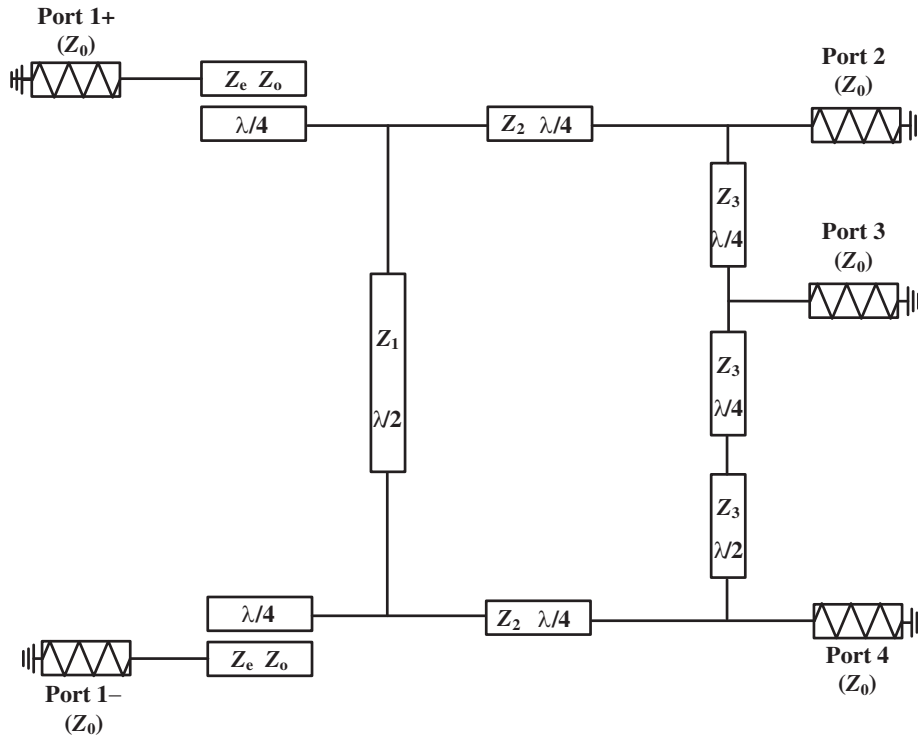


Figure 1. The initial circuit of the proposed BTU Bagley power divider.

be defined as [16]

$$\mathbf{S}_{\text{mm}} = \begin{bmatrix} S_{\text{dd}11} & S_{\text{dc}11} & S_{\text{ds}12} & S_{\text{ds}13} & S_{\text{ds}14} \\ S_{\text{cd}11} & S_{\text{cc}11} & S_{\text{cs}12} & S_{\text{cs}13} & S_{\text{cs}14} \\ S_{\text{sd}21} & S_{\text{sc}21} & S_{\text{ss}22} & S_{\text{ss}23} & S_{\text{ss}24} \\ S_{\text{sd}31} & S_{\text{sc}31} & S_{\text{ss}32} & S_{\text{ss}33} & S_{\text{ss}34} \\ S_{\text{sd}41} & S_{\text{sc}41} & S_{\text{ss}42} & S_{\text{ss}43} & S_{\text{ss}44} \end{bmatrix} \quad (1)$$

and

$$\begin{cases} S_{\text{dd}11} = (S_{1+1+} - S_{1+1-} - S_{1-1+} + S_{1-1-})/2 \\ S_{\text{cc}11} = (S_{1+1+} + S_{1+1-} + S_{1-1+} + S_{1-1-})/2 \\ S_{\text{cd}11} = (S_{1+1+} - S_{1+1-} + S_{1-1+} + S_{1-1-})/2 \\ S_{\text{dc}11} = (S_{1+1+} + S_{1+1-} - S_{1-1+} - S_{1-1-})/2 \end{cases} \quad (2a)$$

$$\begin{cases} S_{\text{sd}n1} = (S_{n1+} - S_{n1-})/\sqrt{2} \\ S_{\text{ds}1n} = (S_{1+n} - S_{1-n})/\sqrt{2} \\ S_{\text{sc}n1} = (S_{n1+} + S_{n1-})/\sqrt{2} \\ S_{\text{cs}1n} = (S_{1+n} + S_{1-n})/\sqrt{2} \end{cases} \quad (2b)$$

$$S_{\text{ss}ij} = S_{ij} \quad (2c)$$

where the subscripts d, c, and s represent differential mode, common mode, and single-ended ports. In order to meet the requirements of ideal impedance matching, mode-conversion coefficient, and common-mode suppression, the following conditions (3) need to be realized.

$$S_{\text{dd}11} = S_{\text{dc}11} = S_{\text{sc}21} = S_{\text{sc}31} = S_{\text{sc}41} = 0 \quad (3a)$$

$$S_{\text{cc}11} = 1 \quad (3b)$$

2.1. Analysis of DM Impedance Matching

As shown in Fig. 1, the block diagram is composed of five transmission lines ($Z_1, \lambda/2$), ($Z_2, \lambda/4$) and ($Z_3, \lambda/4$), and two coupled lines with the even- and odd-mode characteristic impedances of Z_e and Z_o . To compensate the phase difference of the differential input, an additional $\lambda/2$ transmission line (Z_3) is added between ports 3 and 4.

Since the unbalanced port section is not a symmetric structure, the section before the unbalanced port is analyzed first, and this section is considered as a balanced-to-balanced filter, as shown in Fig. 2(a). When the odd-mode (i.e., differential-mode) signal is excited from ports 1+ and 1- in Fig. 2(a), the simplified odd-mode equivalent circuit is shown in Fig. 2(b). The A -matrix between ports 1+ and 2 of the odd-mode equivalent circuit can be calculated as

$$[A] = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} = [A]_{\text{CL}} \times [A]_{\text{TL1}} \times [A]_{\text{TL2}} \quad (4)$$

where

$$[A]_{\text{CL}} = \begin{bmatrix} \frac{c \cos \theta}{d} & j \frac{d^2 - c^2 \cos^2 \theta}{2d \sin \theta} \\ j \frac{2 \sin \theta}{d} & \frac{c \cos \theta}{d} \end{bmatrix} \quad (5)$$

$$c = Z_e + Z_o, \quad d = Z_e - Z_o, \quad \theta = \frac{\pi f}{2f_0} \quad (6)$$

$$[A]_{\text{TL1}} = \begin{bmatrix} 1 & 0 \\ \frac{1}{jZ_1 \tan \theta} & 1 \end{bmatrix} \quad (7)$$

$$[A]_{\text{TL2}} = \begin{bmatrix} \cos \theta & jZ_2 \sin \theta \\ \frac{j \sin \theta}{Z_2} & \cos \theta \end{bmatrix} \quad (8)$$

By applying Equations (5)–(8) into Equation (4), the final A -matrix of odd-mode equivalent circuit at the center frequency f_0 can be expressed as

$$[A] = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} = \begin{bmatrix} \frac{-d}{2Z_2} & j\frac{Z_2 c}{d} \\ 0 & \frac{-2Z_2}{d} \end{bmatrix} \quad (9)$$

According to the transformation relationship between S -matrix and A -matrix, the S -parameter expressed in A -parameters is

$$S_{\text{dd11}} = \frac{A_{11} + \frac{A_{12}}{Z_0} - A_{21}Z_0 - A_{22}}{A_{11} + \frac{A_{12}}{Z_0} + A_{21}Z_0 + A_{22}} \quad (10)$$

where Z_0 is the port impedance. Combining Equations (9) and (10), the final S_{dd11} of the whole circuit in Fig. 2(a) can be obtained as

$$S_{\text{dd11}} = -\frac{4Z_0Z_2^2 - d^2Z_0 + cZ_2^2}{4Z_0Z_2^2 + d^2Z_0 - cZ_2^2} \quad (11)$$

In order to meet the requirement of input impedance matching (i.e., $S_{\text{dd11}} = 0$), the following relationship can be obtained as

$$Z_2^2(4Z_0 + c) = d^2Z_0 \quad (12)$$

By applying Equations (6) into (12), we can get

$$Z_2^2(4Z_0 + Z_e + Z_o) = (Z_e - Z_o)^2 Z_0 \quad (13)$$

So the values of Z_e , Z_o , and Z_2 are selected appropriately to meet the input impedance matching of the differential mode signal.

When the even-mode (i.e., common-mode) signal is excited from ports 1+ and 1– in Fig. 2(a), the simplified even-mode equivalent circuit is shown in Fig. 2(c). The A -matrix between ports 1+ and 2 of the even-mode equivalent circuit can be calculated as

$$\begin{bmatrix} A & B \\ C & D \end{bmatrix} = \begin{bmatrix} \frac{c \cos \theta}{d} & j\frac{d^2 - c^2 \cos^2 \theta}{2d \sin \theta} \\ j\frac{2 \sin \theta}{d} & \frac{c \cos \theta}{d} \end{bmatrix} \begin{bmatrix} 1 & 0 \\ \frac{1}{-jZ_1 \cot \theta} & 1 \end{bmatrix} \begin{bmatrix} \cos \theta & jZ_2 \sin \theta \\ \frac{j \sin \theta}{Z_2} & \cos \theta \end{bmatrix} \quad (14)$$

According to the transformation relationship between S -matrix and A -matrix, we can get

$$S_{\text{cc11}} = \frac{Z_0Z_1d^2 - 4Z_0Z_1Z_2^2 - 2Z_0Z_2^2c + Z_0Z_2d^2 + jZ_2^2d^2 \tan \theta}{Z_0Z_1d^2 + 4Z_0Z_1Z_2^2 + 2Z_0Z_2^2c + Z_0Z_2d^2 + jZ_2^2d^2 \tan \theta} \quad (15)$$

At the center frequency f_0 , $\tan \theta = \infty$, then $S_{\text{cc11}} = 1$, the common-mode signal is completely reflected.

2.2. Analysis of Power Dividing Ratio

To analyze the power dividing ratio, a simplified BTU BPD is shown in Fig. 3(a). When the balanced port 1 is excited by the differential-mode signals, they have the same amplitude and opposite phases. Moreover, it is assumed that all output ports have the same port impedance of Z_0 , and the input impedance at port 1 is Z_x . When the signal passes through the $\lambda/2$ transmission line, the phase is inverted, and the impedance remains unchanged (i.e., $V_4 = -V_5$), so the schematic in Fig. 3(a) is simplified as shown in Fig. 3(b). It is assumed that the powers at port 2 and port 4 in Fig. 3(b) are always equal (i.e., $V_2 = V_4$), then this symmetry helps to draw the equivalent circuit as shown in

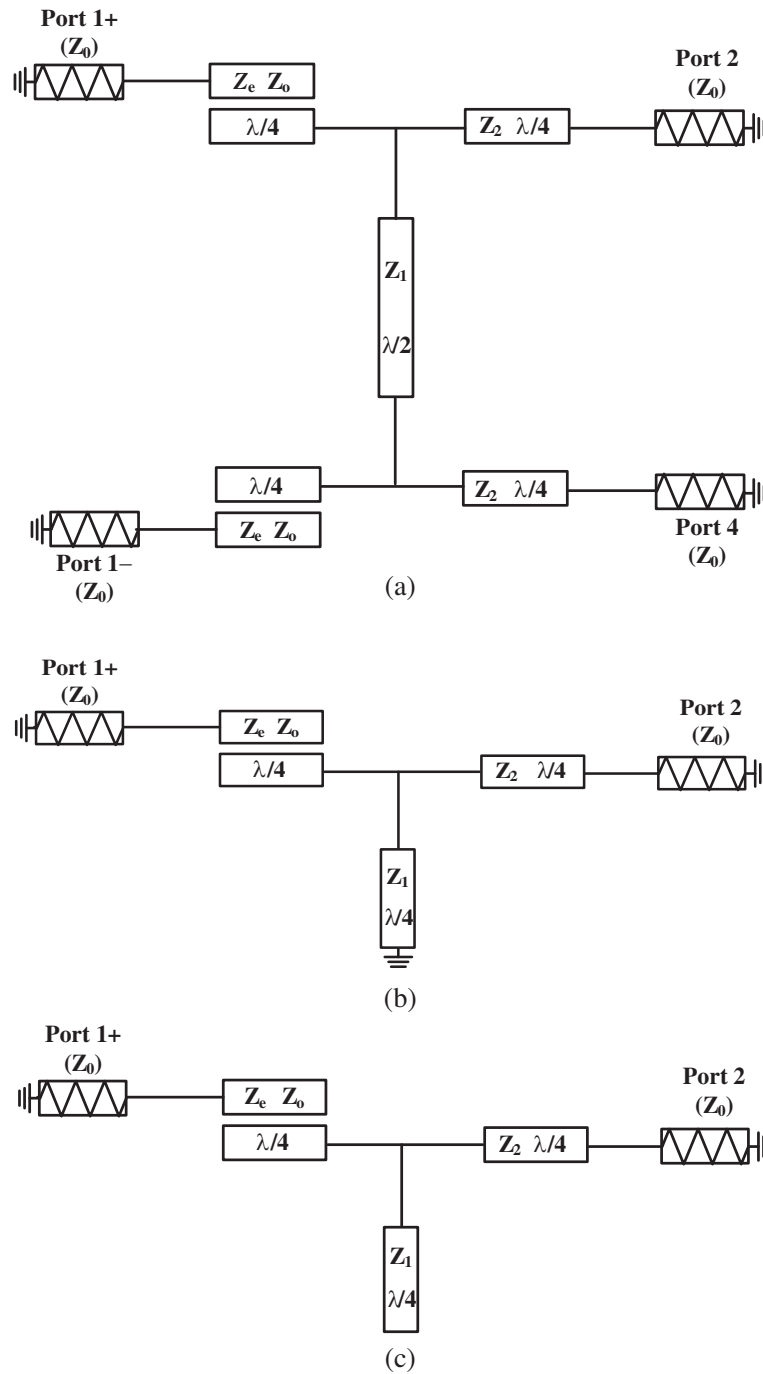


Figure 2. (a) Schematic of the equivalent balanced-to-balanced filter. (b) Odd-mode circuit. (c) Even-mode circuit.

Fig. 3(c). To analyze the equivalent BPD, Kirchoff's current law (KCL) equation for each port (node) is first derived. Applying KCL at port 2,

$$I_2 = I'_2 + I''_2 \tag{16}$$

Substitute the branch current equation into Equation (16) to obtain

$$\frac{V_1}{Z_x} = \frac{jV_3}{Z_3} + \frac{V_2}{Z_0} \tag{17}$$

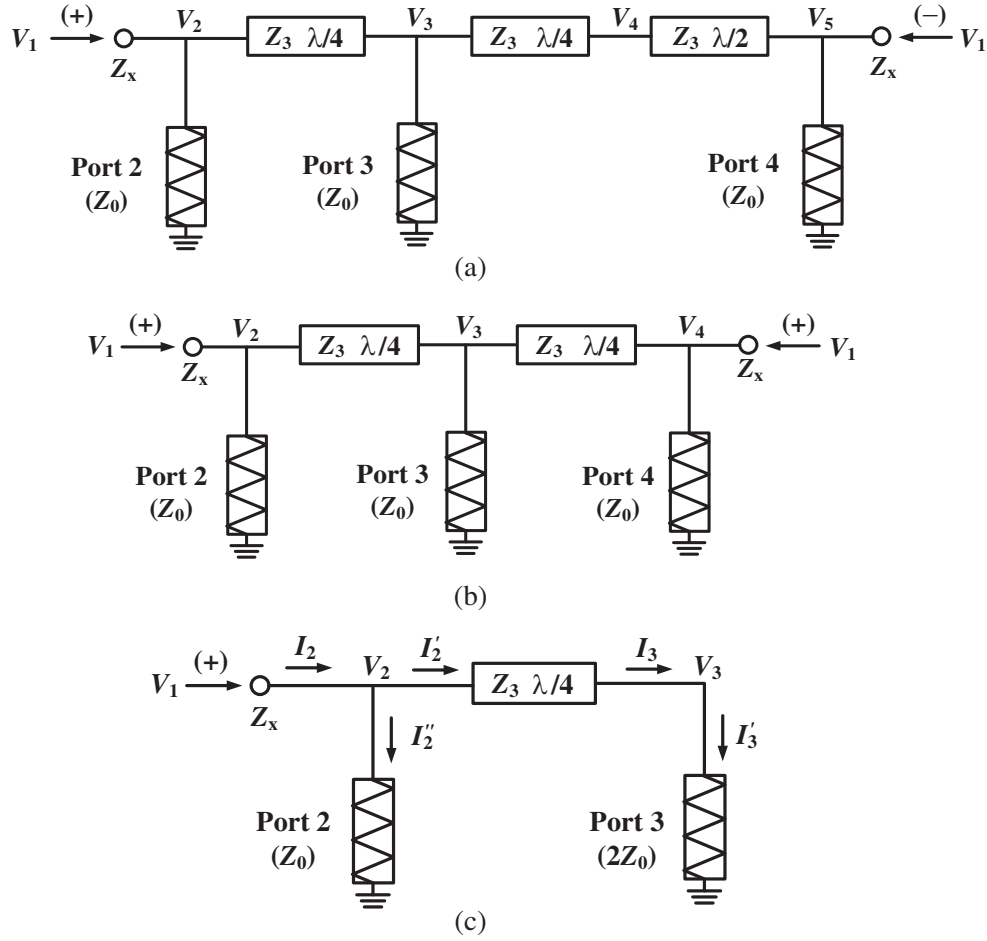


Figure 3. (a) BTU BPD. (b) Simplified circuit. (c) Equivalent circuit.

where

$$Z_x = \frac{Z_0 Z_3^2}{2Z_0^2 + Z_3^2} \quad (18)$$

Likewise, using KCL at port 3,

$$I_3 = I_3' \quad (19)$$

The branch current equation can be substituted in Equation (19) to obtain Equation (20).

$$-\frac{jV_2}{Z_3} = \frac{V_3}{2Z_0} \quad (20)$$

Equations (18) and (20) are used to derive the transmission coefficients of the BPD in question, from which the following equations are derived.

$$S_{21} = \frac{V_2}{V_1} = \frac{Z_0 Z_3^2}{Z_x (2Z_0^2 + Z_3^2)} \quad (21)$$

$$S_{31} = \frac{V_3}{V_1} = -j \frac{2Z_0^2 Z_3}{Z_x (2Z_0^2 + Z_3^2)} \quad (22)$$

When port 1 inputs a unit power, the output powers are as follows,

$$P_2 = P_4 = |S_{21}|^2 = \frac{Z_0^2 Z_3^4}{Z_x^2 (2Z_0^2 + Z_3^2)^2} \quad (23)$$

$$P_3 = |S_{31}|^2 = \frac{4Z_0^4 Z_3^2}{Z_x^2 (2Z_0^2 + Z_3^2)^2} \tag{24}$$

At last, the power dividing ratio of BTU BPD is obtained as follows,

$$P_2 : P_3 : P_4 = 1 : 4 \left(\frac{Z_0}{Z_3} \right)^2 : 1 \tag{25}$$

Note that the power dividing ratio controlled by the Z_3/Z_0 is only available near the center frequency, due to the properties of the $\lambda/4$ transmission line used in Fig. 3.

2.3. Enhancement of Filtering Characteristics

The schematic of the proposed BTU BPD with input-reflectionless filtering characteristics is depicted in Fig. 4. Compared with the BTU BPD shown in Fig. 1, it has two additional blocks. Fig. 5 depicts the simulated mixing S -parameters of the BTU BPD without blocks 1 and 2. When the center frequency $f_0 = 1$ GHz and the coupled lines have even- and odd-mode characteristic impedances of $Z_e = 140 \Omega$ and $Z_o = 60 \Omega$, S_{sd31} has two transmission zeros located at 0.49 and 1.51 GHz, respectively. This attributes to the signal interference of two upper and lower paths [26]. However, the filtering performances for S_{sd21} and S_{sd41} need to be further enhanced.

To improve the passband selectivity, transmission zeros are produced by two stepped-impedance resonators (SIRs) with characteristic impedance and length of $(Z_4, \lambda/4)$ and $(Z_5, \lambda/4)$ (i.e., Block 1 depicted by dash lines in Fig. 4), which are linked to the two unbalanced ports (i.e., ports 2 and 4). Fig. 6 shows the impacts of the proposed BTU BPD with and without stepped impedance resonators. It can be seen that SIR has only a slight effect on the in-band performance. By choosing the proper characteristic impedances Z_4 and Z_5 , high selectivity and out-of-band rejection can be achieved. Furthermore,

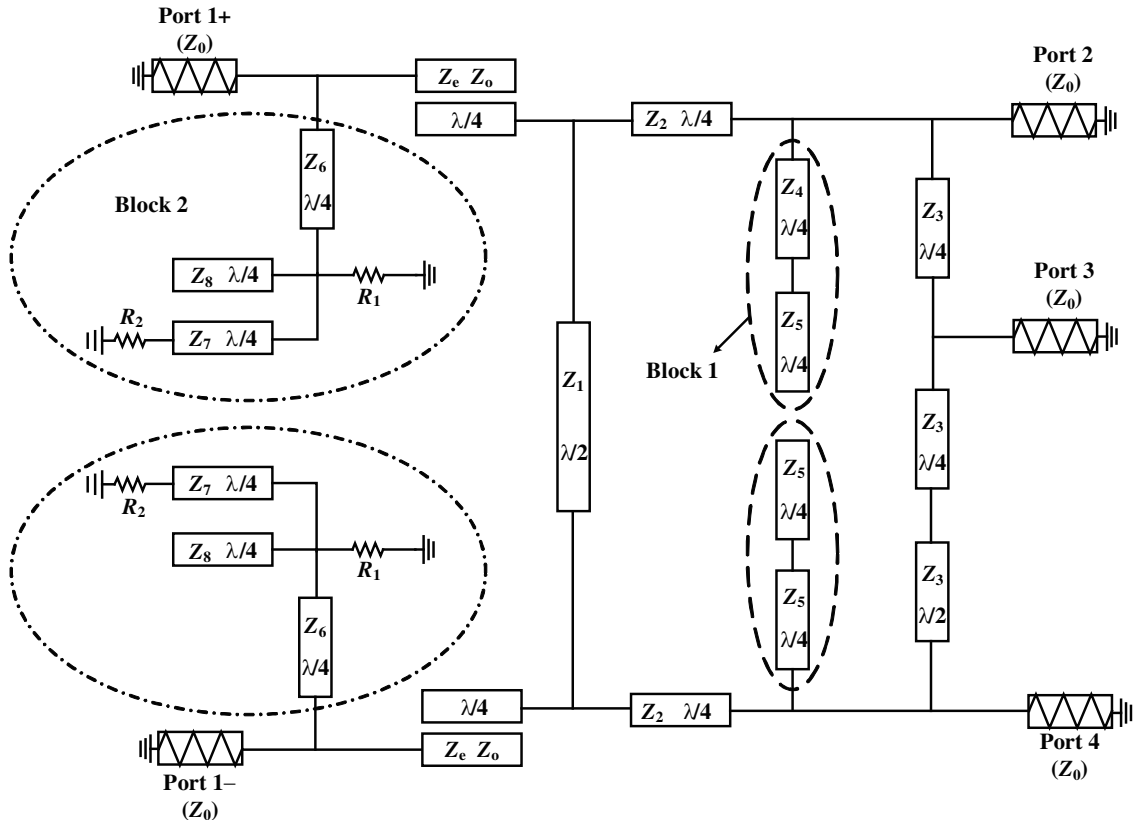


Figure 4. Schematic of the proposed BTU BPD with input-reflectionless filtering characteristics.

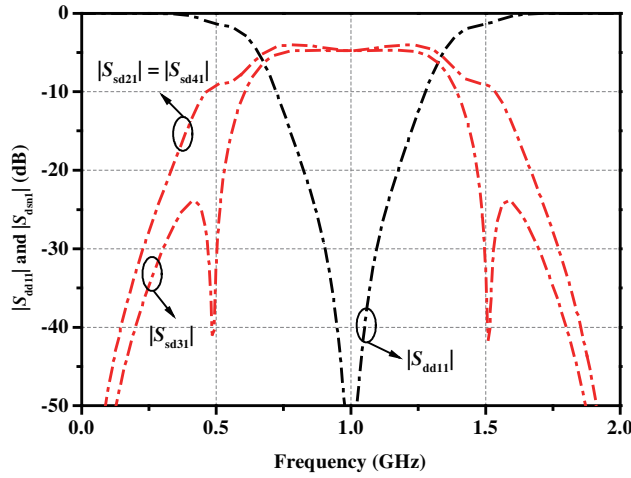


Figure 5. The simulated mixed S -parameters of the BTU BPD shown in Fig. 1.

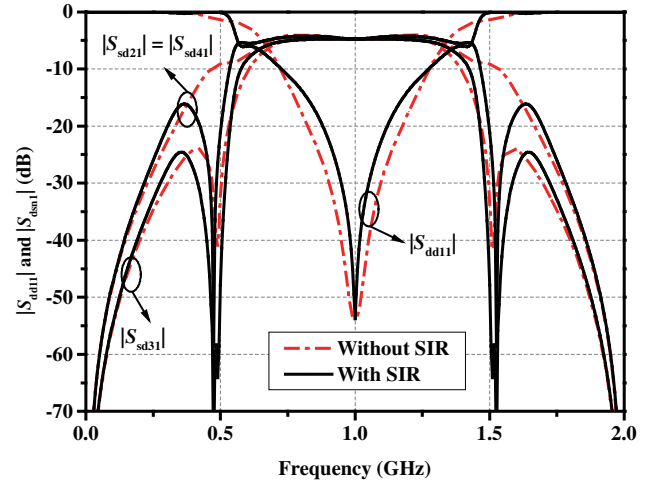


Figure 6. Simulated performances of the proposed BTU BPD with or without the SIR.

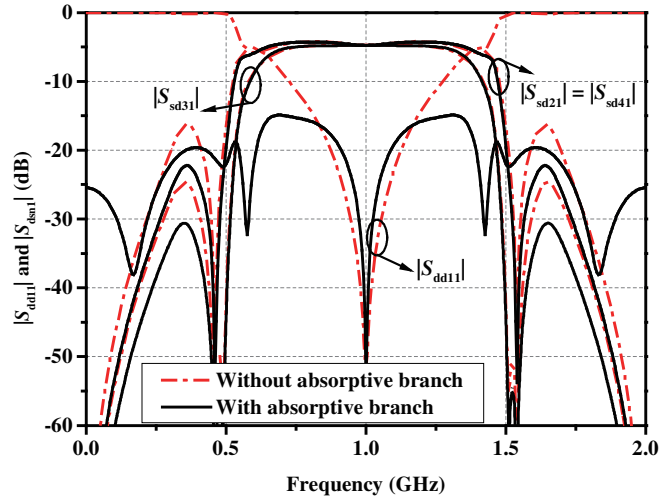


Figure 7. Simulated performances of the proposed BTU BPD with or without the absorptive branches.

considering the fabrication limitation of the width of the transmission lines, the transmission line impedance is within the range of $30 \sim 150 \Omega$.

The effects of the proposed BTU BPD with and without absorptive branches are depicted in Fig. 7. To provide a reflectionless response at the input ports, two absorptive branches (i.e., Block 2 depicted by dash dot lines in Fig. 4) are linked to the balanced ports (i.e., ports 1+ and 1-). Each absorptive branch comprises two $\lambda/4$ transmission lines (Z_6 and Z_7), a $\lambda/4$ open stub (Z_8), and two absorbing resistors (R_1 and R_2). Because of the presence of the absorptive branches, some additional reflection zeros are created. It improves the matching bandwidth of $|S_{dd11}|$, which is shown in Fig. 7. In addition, the added absorptive branch not only does not affect the location of the two transmission zeros, nor does it affect the in-band performance of the differential-mode transmission coefficient, but also increase the out-of-band rejection capability.

2.4. Design Procedure

Based on the above discussions and analysis, a simple design procedure of the proposed BTU BPD with input-reflectionless filtering characteristics can be summarized as follows.

- 1) Determine the power dividing ratio and port impedance, and calculate the value of Z_3 from Equation (25).
- 2) Calculate the value of Z_x from Equation (18), and subsequently select $Z_2 = Z_x$.
- 3) Calculate the values of Z_e and Z_o according to Equation (13) for obtaining ideal impedance matching characteristics at f_0 .
- 4) Specify the filtering selectivity, and select the appropriate Z_4 and Z_5 for stepped impedance resonators.
- 5) Combining the input-reflectionless bandwidth, in-band return loss, and differential-mode out-of-band suppression, determine the absorptive branches.

3. IMPLEMENTATION AND PERFORMANCE

For verification, a microstrip BTU Bagley equal power divider with input-reflectionless filtering characteristics and the center frequency $f_0 = 1$ GHz is designed, fabricated, and measured. Based on the earlier analysis, the values of the electrical parameters obtained are shown in Table 1. The design is a planar structure, which can be fabricated using a simple single-layer printed circuit board, and its photograph is shown in Fig. 8. After optimization by Ansoft HFSS software, the final parameters are listed in Fig. 8(a). As depicted in Fig. 8(b), the prototype is realized with microstrip lines and fabricated on an F4B substrate with a thickness of 1.5 mm, dielectric constant of 2.65, and loss tangent of 0.003. The fabricated prototype is tested using an Agilent N5230A network analyzer.

Table 1. Electrical parameters of the proposed BTU BPD with input-reflectionless filtering characteristics.

Z_e (Ω)	Z_o (Ω)	Z_1 (Ω)	Z_2 (Ω)	Z_3 (Ω)	Z_4 (Ω)
140	60	36	33	100	115
Z_5 (Ω)	Z_6 (Ω)	Z_7 (Ω)	Z_8 (Ω)	R_1 (Ω)	R_2 (Ω)
70	82	145	45	76	110

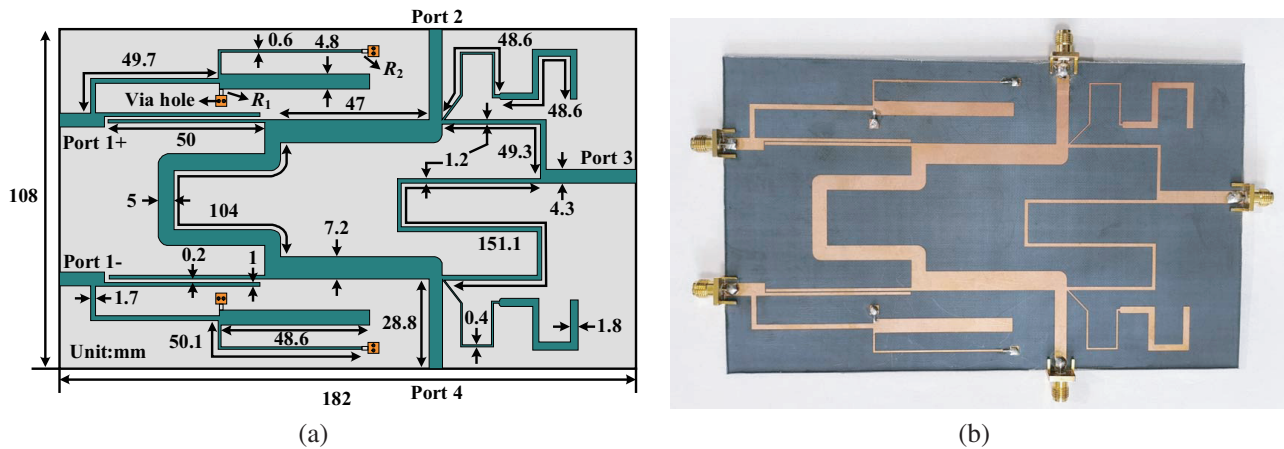


Figure 8. (a) Layout and (b) photograph of the fabricated BTU BPD with input-reflectionless filtering characteristics.

Simulation and measurement curves of the proposed BTU BPD with input-reflectionless filtering characteristics in the 0 ~ 2.5 GHz range are given in Fig. 9. For the balanced-port responses, the differential-mode return loss is greater than 10 dB (i.e., $|S_{dd11}| < -10$ dB) at all frequencies, achieving wideband input-reflectionless characteristics in Fig. 9(a). In addition, the measured mode-conversion coefficient $|S_{dc11}|$ is less than -20 dB. Fig. 9(b) is the measured and simulated results under DM signal

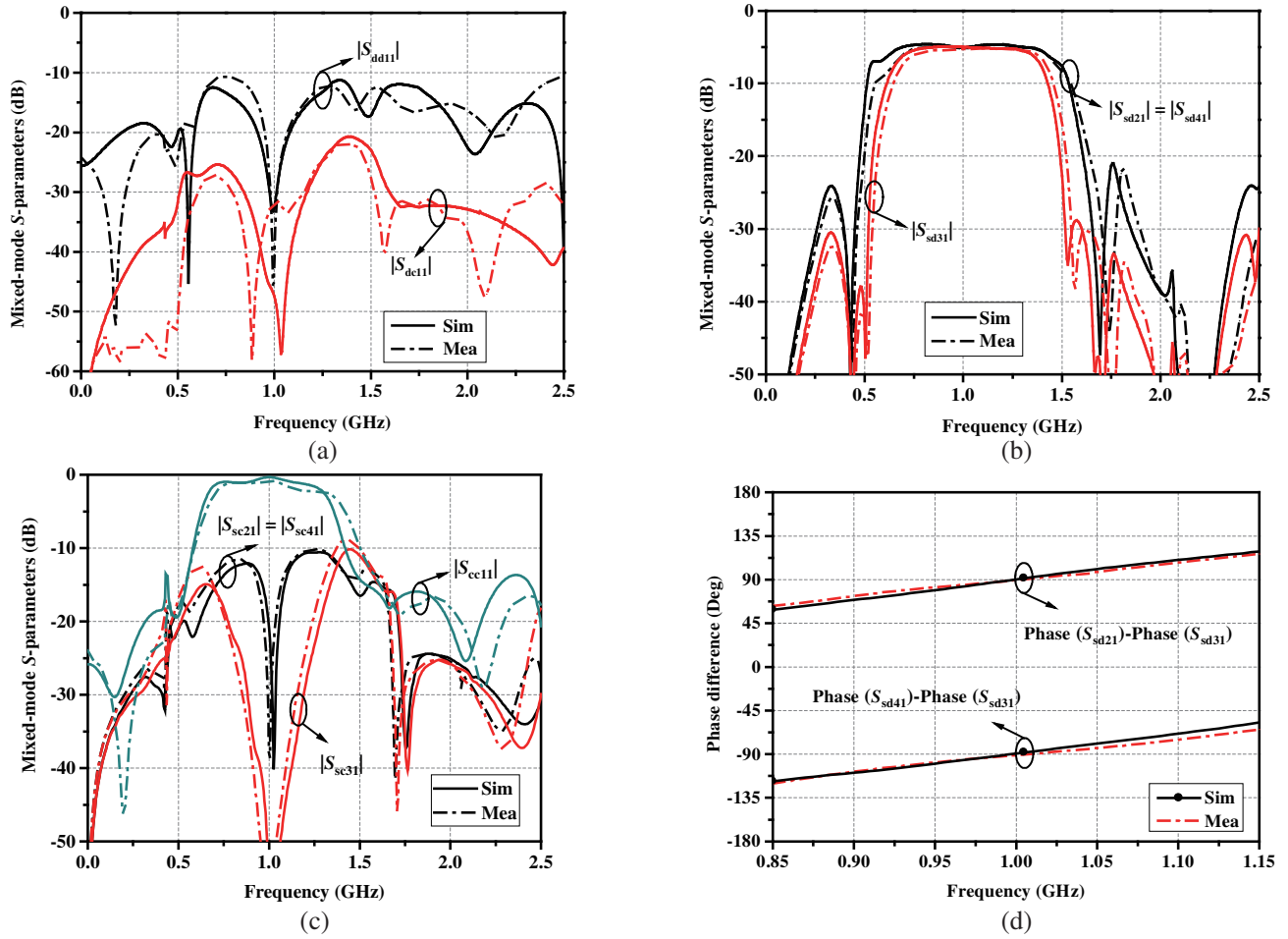


Figure 9. Simulated and measured results of the proposed BTU BPD with input-reflectionless filtering characteristics. (a) Balanced-port responses. (b) DM responses. (c) CM responses. (d) Phase difference.

excitation at the balanced port 1. The insertion loss including the power division loss for the three outputs is 5.2 ± 0.3 dB, and the measured 3-dB filtering bandwidth (FBW) is 72% ($0.675 \sim 1.435$ GHz). DM to 3-port transmission coefficient $|S_{sd31}|$ introduces four transmission zeros at 0.44, 0.51, 1.56, and 1.76 GHz, respectively, DM to 2-port (4-port) transmission coefficient $|S_{sd21}|$ ($|S_{sd41}|$) introduces two transmission zeros at 0.44 and 1.74 GHz, respectively, improving the filtering performance. Meanwhile, the DM out-of-band rejection is better than 20 dB. For the CM signal excitation in Fig. 9(c), the measured CM to 3-port transmission coefficient $|S_{sc31}|$ is less than -8.9 dB with the greatest 60-dB CMS at the center frequency, and the measured CM to 2-port (4-port) transmission coefficient $|S_{sc21}|$ ($|S_{sc41}|$) is less than -10 dB with the greatest 47-dB CMS at the center of frequency. In the stopband, the measured CM reflection coefficient $|S_{cc11}|$ is below -10 dB from 0 to 0.58 GHz and from 1.5 to 2.5 GHz. As shown in Fig. 9(d), the measured and simulated phase differences between the output ports 2 and 3 at f_0 are $90^\circ \pm 1^\circ$, and the phase differences between the output ports 3 and 4 are $-90^\circ \pm 1^\circ$.

The comparisons of the proposed BTU BPD with previous works are summarized in Table 2. This work is the first design of a BTU input-reflectionless filtering Bagley power divider. Compared with the reported power divider, the proposed BTU BPD has a wide filtering band and a good CM noise suppression level. Furthermore, input-reflectionless filtering characteristics have been integrated into the designed structure, which can protect the active stages in the RF front ends from undesired RF power reflection interference.

Table 2. Performance comparisons between the proposed BTU BPD and previous power divider.

Reference	f_0 (GHz)	Type	Number of output ports	FBW (%)	CMS (dB)	Input- reflectionless filtering
[4]	6.3	Single-ended Bagley	3	121.6	-	No
[9]	3.5	BTU DR	2	2.2	20	No
[14]	2.0	BTU Wilkinson	2	25	40	No
[15]	1.8	BTU Gysel	2	37.2	32	No
[16]	2.0	BTU Gysel	2	80	40	No
[17]	4.0	BTU Gysel	2	47.2	35	No
[24]	1.2	Single-ended Wilkinson	2	7.5	-	Yes
This work	1.0	BTU Bagley	3	72	47	Yes

DR: dielectric resonator.

4. CONCLUSIONS

In this paper, a novel BTU BPD with input-reflectionless filtering characteristics has been presented. The wideband input-reflectionless characteristics for the balanced port with DM excitation have been achieved by loading absorptive branches, where the differential-mode return loss is greater than 10 dB at all the measurement frequencies from 0 to 2.5 GHz. By using the stepped impedance resonator, two transmission zeros can be obtained near the passband to further enhance the filtering selectivity. Compared with the existing BPDs, the advantages of the proposed BTU BPD are DM signal transmission, good CMS, wideband input-reflectionless filtering, and the ability to connect to single-ended and balanced circuits. Furthermore, it can be applied in sequential three-feed circularly-polarized patch antenna [27] and three-way sequential power amplifier [28, 29] to add input-reflectionless filtering function and suppress the common-mode interference.

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