Design of a Broadband Fixed IF Sub-Harmonic Mixer at Ka Band

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Abstract—This paper describes the design of a broadband, fixed-IF, high efficiency single subharmonic mixer at Ka-band. The co-simulation between HFSS and ADS is applied to the modeling of the mixer. In order to improve the accuracy of simulation, the diode model is divided into passive linear model and active nonlinear model. On this basis, a global accurate equivalent circuit model of mixer is proposed and verified by testing data. The circuit of the presented mixer printed on the substrate of Rogers RT/Duroid 3003 is mounted in a waveguide block. When the fixed IF frequency is set at 1.5 GHz, measured results show that the conversion loss is less than 8 dB over the RF bandwidth from 25 GHz to 39 GHz with 12 dBm of local oscillator power. The minimum conversion loss of 6.2 dB is measured at 28 GHz. The measured isolation between LO and IF, LO and RF is over 23 dB. The measured isolation between IF and RF is over 20 dB. Good isolation is achieved.

1. INTRODUCTION

Millimeter wave band like Ka-band is widely used in the field of satellite communications for its wide bandwidth and fast communication speed [1]. The mixer is an important part of radar and microwave measurement systems. In the measurement systems, the frequency of IF signal is usually fixed. Therefore, the back-end circuit can be well designed. Most mixers with fixed frequency LO, the frequency of IF signals changes with RF signals. However, the proposed mixer in this work has fixed IF frequency and broadband matching of LO and RF. The IF port can be sampled directly by back-end circuit. Additionally, it can avoid second times of down-conversion and further reduce the volume of the systems.

Harmonic mixers require LO signals of only 1/N the frequency of the RF (where N is the harmonic order) [2]. It can ensure good isolation between RF and LO. However, conversion loss increases compared with fundamental mixers. Considering that the low fixed-IF frequency is set at 1.5 GHz, to obtain better isolation and low conversion loss, sub-harmonic mixer becomes a better choice. The local oscillator frequency of Ka-band sub harmonic mixer is located at Ku-band, and the waveguide size of this band is larger. To ensure good broadband matching and reduce module volume, substrate integrated waveguide (SIW) microstrip transition is adopted. SIW can realize the propagation characteristics of the traditional metal waveguide on the substrate, and it has many advantages such as low radiation, low insertion loss, high Q value, high power capacity and small volume for rectangular waveguide and microstrip device. Through the good matching networks of RF and LO, the conversion loss is less than 8 dB over the bandwith from 26 GHz to 39 GHz when fixed IF is 1.5 GHz. Therefore, characteristic of wide band and high efficiency can be achieved.

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2. MIXER DESIGN

The anti-parallel balanced circuit is shown in Fig. 1. These two diodes are anti-parallel as to input and output circuits which make the odd-harmonic mixing components exist in the internal circulation of the diode pair, while even-harmonic mixing components exist outside. The suppression to odd harmonics depends on the uniformity of the diodes and the symmetry of the circuit.

Accurate diode equivalent circuit is the key of mixer design [3]. The diode chip used in this work is MA4E1310 produced by MACOM. In millimeter wave band, the geometry of the diode chip causes significant high frequency parasitic effect and affects the performance of the mixer greatly. This work divides diode model into passive linear model and active nonlinear equivalent model. Important parameters of the active nonlinear equivalent circuit model are obtained through the datasheet. It is built in ADS. Then, three-dimensional (3D) EM model of diode chip is built in High Frequency Structure Simulator (HFSS). The S-parameter extracted from 3D EM model can describe the high frequency parasitic characteristic. The active nonlinear equivalent circuit in ADS shows the nonlinear behavior of the Schottky junction. Fig. 2 shows the diode nonlinear equivalent model. Fig. 3 shows the 3D EM diode model mounted on the pad.

The principle circuit of the subharmonic mixer is shown in Fig. 4. The mixing circuit consists of an RF probe [4], an LO SIW transition [5], an idle energy recovery network [6], an IF filter, the matching networks, a compensation solder pad and a pair of Schottky diodes.

To minimize the loss of the RF signal and achieve broadband matching in Ka-band, the WR-28 waveguide and 1/4 wavelength microstrip line matching networks are used. The LO signal is fed by SIW transition because the size of standard waveguide Ku-band is large 15.799 mm * 7.899 mm. It has





Figure 1. The anti-parallel balanced Figure 2 circuit.

Figure 2. The diode nonlinear equivalent model.



Figure 3. (a) Optical microscope image of diode, (b) 3D model of diode.



Figure 4. Structure of the mixer.



Figure 5. (a) Exponential microstrip gradient structure, (b) simulated results.

advantages of both rectangular waveguide and microstrip device to achieve broadband matching of the LO. In addition, the volume of circuit can also be reduced. The transition of SIW with microstrip is one of the principal problems in studying SIW. In order to heighten the energy transmission efficiency between them, one exponential microstrip gradient structure is designed by the analysis of the impedance matching of them. The structure is shown in Fig. 5. The exponential function is $y = Ae^{m(x-x_0)}$. From Fig. 5, $A = w_s/2$, $x_0 = L/2$. The performance of the structure is greatly influenced by the transition length "Ls". The exponential coefficient "m" affects the width of microstrip line " w_s ". The simulation results are shown in Fig. 5. The simulated return loss is better than 25 dB from 12 GHz to 20 GHz, which can ensure great energy transmission efficiency.

By designing a reasonable idle frequency energy recovery circuit at the LO, the idle energy including the RF energy and odd harmonic energy of the LO is recycled to decrease the conversion loss of the mixer.

The solder pad is used to connect the nonlinear device and linear circuit [7]. However, with the increase of working frequency, the influence of parasitic parameters introduced by pads on the performance of the whole circuit is also affected. In order to improve the design reliability and improve the performance of the mixer, a compensation solder pad for antiparallel diode is developed to compensate the capacitance resulted by the pad of the nonlinear device. By using ADS to extract the impedance of diodes at fundamental and secondharmonic frequency, the broadband matching networks of RF and LO can be reasonably designed.

The filter with BCMRC is designed to get the IF signal from the mixer and ensure good isolation between IF and LO [8,9]. It exhibits remarkable wide stopband and slow-wave characteristics. Simulated results of the filter are shown in Fig. 6. In actual use, these three ports of the mixer have T font distribution to facilitated circuit installation.

The *s*-parameters of the passive circuit models are exported from HFSS and then imported into ADS. In the Advanced Design System, the *s*-parameters of the passive circuit are imported into Harmonic Balance Simulator where we model the nonlinear behavior of the diode pair. Through continuous optimization, the mixer gets the optimum performance.

The output terminal of the filter is extended by 50 Ohm microstrip line so that the three ports have T font distribution. To avoid cavity resonance, two isolation blocks are added to separate the cavity.



Figure 6. The simulated results of IF low pass filter for CMRC.



Figure 7. The fabrication of the proposed circuit. (a) Inside view of the assembled bottom block, (b) the photograph of the mixer.

3. FABRICATION AND MEASUREMENT

The circuit of the presented mixer is patterned on a substrate of Rogers RT/Duroid 3003 with a permittivity of 3 and thickness of $254 \,\mu\text{m}$ followed with a photolithography process. The *E*-Plane cut split-block waveguide is milled in aluminum and the LO and IF part connected to the SMA connector.



Figure 8. Comparison of conversion loss between simulated result and measured result.



Figure 9. Measured result of conversion loss at different IF frequency.

The anti-parallel planar Schottky diodes chip is a flip-chip mounted on the pad with silver epoxy. The mixer is fabricated and shown in Fig. 7. Its overall dimension is $49 * 41 * 10 \text{ mm}^3$.

In the conversion loss test, a coaxial-to-waveguide adapter is used with the RF waveguide. The RF signal is provided by an Agilent E8257D signal generator from 26 GHz to 40 GHz. The LO signal is provided by an RS@SMB100A signal generator, and the IF signal is measured by an Agilent 8257D spectrum analyzer.

The comparison of conversion loss between simulated results and testing results is shown in the Fig. 8. The results show that the testing data and simulated data are consistent. The accuracy

product	mixing style	frequency of RF	frequency of LO	conversion loss	test style
our paper	subharmonic	$2539\mathrm{GHz}$	$11.75 - 18.75\mathrm{GHz}$	$7 - 8 \mathrm{dB}$	fixed IF
paper $[10]$	subharmonic	$3537\mathrm{GHz}$	$35\mathrm{GHz}$	$6.57.5\mathrm{dB}$	fixed LO
paper [11]	subharmonic	$2444\mathrm{GHz}$	$1222\mathrm{GHz}$	$13\text{-}14.5\mathrm{dB}$	fixed IF
paper $[12]$	subharmonic	$2730\mathrm{GHz}$	$8-9.4\mathrm{GHz}$	$8.510.5\mathrm{dB}$	fixed IF
paper $[13]$	subharmonic	$3040\mathrm{GHz}$	$14\mathrm{GHz}$	$810\mathrm{dB}$	fixed LO
paper [14]	subharmonic	$18-26.5\mathrm{GHz}$	$15\mathrm{GHz}$	$7.510.5\mathrm{dB}$	fixed LO

Table 1. Comparison between the performance of this design and other products.

of the diode equivalent circuit has been verified. The above experimental results achieve the design expectation of the Ka band broadband fixed IF sub harmonic mixer. Our design has good performance compared with other products in the same frequency band. Table 1 shows the comparison between the performances of our design and other products.

The measured results of conversion loss are illustrated in Fig. 9 with the LO power level of 12 dBm and the IF frequency fixed from 1 GHz to 2.7 GHz. The results show that the conversion loss is below 15 dB from 25 GHz to 39 GHz when the fixed IF frequency is from 1.1 GHz to 2.6 GHz. The conversion loss is less than 8 dB from 25 GHz to 39 GHz when the IF frequency is fixed at 1.5 GHz and 1.6 GHz.

The measured isolation is illustrated in Fig. 10. Isolation between LO and RF, LO and IF is over 23 dB, Isolation between IF and RF is over 20 dB. Respectively good isolations have been achieved.



Figure 10. Measured isolation.

4. CONCLUSION

With the field-circuit co-simulation method, a broadband, high efficiency, fixed-IF subharmonic mixer at Ka-band is designed in this work, based on the accurate passive linear model and active nonlinear model. The measured results show that the optimum conversion loss of the mixer is less than 8 dB over the bandwidth from 25 GHz to 39 GHz when the IF frequency is fixed at 1.5 GHz. The measured results are in accordance with the simulation results. The accuracy and feasibility of the model discussed are verified in this article. A reasonable matching network and idle energy recovery network are adopted to achieve good isolation among the three ports and prevent port power leakage.

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