A Novel Z-EBG Structure Embedded by DBCSRR for Suppression of Simultaneous Switching Noise

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Abstract—Aimed at solving the problems of high initial cutoff frequency, small stopband range and poor inhibition in the current electromagnetic band gap (EBG) structure, an electromagnetic band gap structure designed on the basis of periodic Z-bridge EBG inserted by a double complementary slit ring resonator (DBCSRR) cell is proposed. Compared with the traditional EBG structure, the proposed EBG structure can achieve 270 MHz–20 GHz bandwidth in a reference of $-30 \, \text{dB}$, which is wide in range. The measured and simulated results indicate the wideband of noise suppression. In addition, the lower and upper cutoff frequencies are estimated by using equivalent circuit models, respectively. Moreover, the IR-Drop and dc resistance is accurately investigated through 3-D simulations. Finally, the transfer characteristics of single signal line are studied.

1. INTRODUCTION

With the rapid development in integrated electronic systems, the clock frequency has reached at GHz level with faster transmission speed of signals, and data up and down time factors have been much steeper for the modern integrated circuits. The increase in high-speed clock frequency introduces serious power integrity (PI) problems and ground bounce noise (GBN), especially the inevitable Simultaneous Switching Noise (SSN) caused by the continuous switching off of a large number of drive switches. SSN can result in a degradation of the signal integrity (SI) and a significant decline in system stability [1–3].

Many noise suppression methods have been developed to mitigate the effect of SSN including using decoupling capacitors, selecting the number and location of the vias and using differential interconnects [4]. However, these methods have some disadvantages that either they cannot provide efficient suppression of noise at high frequency or they are expensive to implement. Hence, a new-type structure called electromagnetic band gap (EBG) structure is brought forward to suppress this kind of noise.

The EBG structure was originally derived from photonic band gap (PBG) structure in optics [5]. Since the EBG structure has the characteristic of prohibiting the propagation of electromagnetic waves within a certain bandwidth, it has wide advantages for SSN suppression such as wider bandwidth and deeper suppression depth. EBG structure has been widely developed in the filter [6,7] and the antenna [8,9].

In recent years, several types of EBG structures have been developed to inhibit SSN. Threedimensional (3-D) embedding structures represented by mushroom-type EBG structure [10–14] can suppress SSN, but the production cost is too high. Coplanar compact EBG structures represented by L-bridge EBG structure [15–17] and other bridge EBG structures are presented, but the bandwidth of suppression is limited.

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A novel Z-EBG structure embedded by a double complementary slit ring resonator (DBCSRR) is proposed in this paper. Based on the previous work, the DBCSRR is embedded in the Z-bridge structure to obtain better suppression of SSN. A wide stopband for the suppression against SSN propagation extending from 270 MHz to 20 GHz at -30 dB is achieved. At the end of the paper, the time-domain eye diagram is used to verify the application of this structure to meet the signal integrity requirements of high-speed circuits.

2. STRUCTURE DESIGN

The new structure is based on the existing Z-bridge EBG structure, by adding DBCSRR structure in some of the cells to improve the SSN suppression capability of the structure. As shown in Fig. 1. The power plane is a graphical EBG plane. It consists of five Z-bridge structural elements embedded with DBCSRR structures and four Z-bridge structural elements. The topography is shown in Fig. 1(a) and the side view shown in Fig. 1(b).

The structure of Z-bridge EBG unit with embedded DBCSRR structure is shown in Fig. 2. The corresponding parameters are $a_1 = 30 \text{ mm}$, $a_2 = 27.4 \text{ mm}$, $a_3 = 22.4 \text{ mm}$, $b_1 = 28.8 \text{ mm}$, $g_1 = 0.5 \text{ mm}$, $g_2 = 0.5 \text{ mm}$, $g_3 = 0.2 \text{ mm}$, $g_4 = 0.2 \text{ mm}$, $g_5 = 1.7 \text{ mm}$, $w_1 = 0.2 \text{ mm}$, $w_2 = 0.4 \text{ mm}$, $w_3 = 0.1 \text{ mm}$, $w_4 = 0.4 \text{ mm}$, $w_5 = 0.4 \text{ mm}$. The substrate material is FR-4 with a relative permittivity 4.4 and loss



Figure 1. The presented novel EBG structure: (a) Top view. (b) Side view.



Figure 2. Unit of Z-bridge EBG embedded DBCSRR structure.

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tangent of 0.02. The thickness of the substrate is 0.4 mm and the thickness of the copper foil is 0.035 mm. To verify the performance of this architecture for SSN noise suppression, four 50 Ω lumped coaxial ports were set up, with Port 1 as the input port and Port 2–Port 4 as the output ports. Each port location is in the center of each cell.

In order to obtain the power supply noise suppression capability of the proposed structure, the structure is simulated by Ansoft HFSS V15, and the amplitude of the S parameter between the required ports is obtained finally. Fig. 3 shows the simulated and measured S-parameters for the designed novel EBG structure. As shown in Fig. 3, the proposed EBG structure has a good behavior that its SSN suppression (S_{21}) is observed starting at approximately 270 MHz and extending to 20 GHz at -30 dB suppression of the band gap depth. It can be seen that the Z-bridge EBG with embedded DBCSRR structure has a good noise suppression effect at -30 dB suppression depth, and the suppression effect is superior to the L-bridge EBG structure and Z-bridge EBG structure. Simultaneously, the simulated result is similar to the measured one as shown in Fig. 3(b) by the vector network analyzer (VNA).



Figure 3. Simulated and measured S-parameter for the designed novel EBG structure. (a) The simulated S-parameter for the designed novel EBG structure. (b) Comparison of the simulation and measurement of S_{21} for proposed structure.

3. THEORETICAL ANALYSIS FOR PROPOSED STRUCTURE

3.1. The Estimate of the Lower Cutoff Frequency f_L

The EBG structure is functionally equivalent to a low-pass filter composed of a plurality of lumped circuit elements, when works in the low hundreds of MHz. These elements are determined by the overall size of the EBG cell. The lumped parameters of these components include the parallel-plate capacitance C_p , wide conductor loop inductance L_p , bridge microstrip capacitance C_b , bridge partial inductance L_b and gap coupling capacitance between the neighboring metal patches C_g . The formulas can be derived as [18–20], respectively:

$$C_p = \varepsilon_0 \varepsilon_r \frac{S}{h} \tag{1a}$$

$$L_p = \mu_0 h \frac{len}{W} \tag{1b}$$

$$C_b = len \cdot \frac{0.67(1.41 + \varepsilon_r)}{\ln\left(\frac{7.5h}{W}\right)}$$
(1c)

$$L_b = len \cdot k \cdot \ln\left(2\pi \frac{h}{W}\right) \tag{1d}$$

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$$C_g = \frac{\varepsilon_0 (1 + \varepsilon_r) l}{\pi} \cos h^{-1} \left(\frac{p}{g}\right)$$
(1e)

where ε_0 and μ_0 are the permittivity and permeability of free space respectively. ε_r is the relative dielectric constant, S the projective area, h the substrate thickness between the power plane and ground plane, *len* the length of the microstrip line, w the width of the line, k a constant 0.2 nH/mm, l the side length of the patch of EBG unit, p the cycle of EBG units, and g the gap between the adjacent units.

The equivalent model consists of three parts. The first part describes the propagation characteristics between the traditional EBG patch and the solid ground plane, represented by the equivalent capacitance $C_{pl} = 73.086 \,\mathrm{pF}$ and inductance $L_{pl} = 0.5 \,\mathrm{nH}$. The second part characterizes the bridge effect, represented by the bridge inductance $L_{bl} = 42.5718 \,\mathrm{nH}$, bridge capacitance C_{bl1} and C_{bl2} , $C_{bl} = 2C_{bl1} + C_{bl2} = 3.52 \,\mathrm{pF}$. And in the third part, C_{bp} and L_{bp} are the equivalent capacitance and equivalent inductance of the inner parallel-plate of the DBCSRR. C_{bbn} and L_{bbn} denote the capacitance and parallel inductance of the coplanar waveguide in the DBCSRR resonator.

$$C_{bp} = \varepsilon_0 \varepsilon_r \frac{d^2}{h} \tag{1f}$$

$$L_{bp} = \mu_0 h \tag{1g}$$

$$C_{bbn} = 4\varepsilon_0\varepsilon_r w_1 (d + 2s + w_1)/h \tag{1h}$$

$$L_{bbn} = (d + 2s + w_1)L_{pul} \tag{1i}$$

 L_{pul} is the inductance of per unit length of the coplanar waveguide.

$$L_{pul} = Z_{\rm CPW} \sqrt{\varepsilon_{effc}} / c \tag{1j}$$

where Z_{CPW} and ε_{effc} are the impedance and effective permittivity of the coplanar waveguide, and c is the velocity of light in free space and d^2 the projective area of DBCSRR resonator. The joint between the novel structure units can be modeled with the equivalent circuit as shown in Fig. 4(a). The equivalent capacitance and equivalent inductance of the inner parallel-plate of the DBCSRR $C_{bp} = 48.846$ pF and $L_{bl} = 42.5718278$ nH, the capacitance and parallel inductance of the coplanar waveguide in the DBCSRR resonator $C_{bbn} = 3.613$ pF and $L_{bbn} = 8.527749$ nH.

The bridge capacitance C_{bl} and gap capacitance between the joint C_g can be ignored for that they are extremely large in the frequency range of interest. So the equivalent circuit can be simplified to a single resonance circuit model, as shown in Fig. 4(b).

As a result, the lower cutoff frequency of the new EBG can be derived from resonance frequency of the parallel LC resonator in Eq. (2).

$$f_L = \frac{1}{\pi} \sqrt{\frac{C_{pl} + C_{bp}}{\left(L_{pl} + \frac{L_{bbn}}{2} + L_{bl}\right) \cdot C_{pl} \cdot C_{bp}}}$$
(2)

Based on Eqs. (1)–(2) and the new EBG structure dimensions, the calculated results are following $f_L = 261.583$ MHz. So f_L is approximate to the simulation on f_L .

3.2. Discussion of the Cutoff Frequency f_H

Similarly, the circuit element L_{bl} can be ignored for analyzing the high cutoff frequency according to Fig. 4, and the impedance of the equivalent circuit can be calculated. The Z can be derived as

$$Z = \left(j\omega\frac{L_{bp}}{2} + \frac{2}{j\omega(C_{bp} + C_g)}\right) \left\| \left(j\omega\frac{L_{bp}}{2} + \frac{2}{j\omega(C_{pl} + C_g)}\right)$$
(3)

$$Z = \frac{-\omega^4 L_{pb}^2 (C_{bp} + C_g) (C_{pl} + C_g) + 4\omega^2 (C_{bp} + C_{pl} + 2C_g) - 16}{4j \left[\omega^3 L_{bp} (C_{bp} + C_g) (C_{pl} + C_g) - 2\omega (C_{bp} + C_{pl} + 2C_g)\right]}$$
(4)

where $Z = \frac{P(\omega)}{Q(\omega)}$ and

$$Q(\omega) = 4j \left[\omega^3 L_{pb} (C_{pb} + C_g) (C_{pl} + C_g) - 2\omega (C_{pb} + C_{pl} + 2C_g) \right]$$
(5)



Figure 4. Equivalent circuit for the joint between Z-bridge EBG structure units. (a) Complete model. (b) Lower cutoff frequency simplified model. (c) Higher cutoff frequency simplified model.

If Z tends to infinity and $Q(\omega) = 0$, as

$$\omega^2 L_{pb} (C_{pb} + C_g) (C_{pl} + C_g) - 2(C_{pb} + C_{pl} + 2C_g) = 0$$
(6)

$$\omega = \sqrt{\frac{2(C_{pb} + C_{pl} + 2C_g)}{L_{pb}(C_{pb} + C_g)(C_{pl} + C_g)}} \tag{7}$$

$$f_H = \frac{1}{2\pi} \sqrt{\frac{2(C_{pb} + C_{pl} + 2C_g)}{L_{pb}(C_{pb} + C_g)(C_{pl} + C_g)}} = 18.7 \,\text{GHz}$$
(8)

3.3. Analysis of IR-Drop

The dc current flow causes a voltage drop (known as IR-drop) across the EBG plane [21]. And the IRdrop of a planar EBG plane needs to be considered when it is used for the power distribution network (PDN). The dc resistance of the proposed structure is larger than that of the integrated power and ground plane because of the Z-bridges [22]. Therefore, the IR-drop of the proposed EBG is evaluated across the patterned plane by using CST EM Studio in this paper.

The proposed EBG simulates the worst example that the distance between the two ports is farthest. It is able to calculate the voltage distribution on the plane by adding two perfect electric conductor (PEC) auxiliary bricks for injecting the 1 A DC current [15]. Figs. 5(a) and (b) show the dc-drop between the current ports on the power plane and the ground plane, respectively. According to the results of the simulation, the overall voltage differences on the power plane and the ground plane are 145.725 mV and 0.545 mV, respectively, which corresponding to the dc resistance are 145.725 mQ and 0.545 mQ based on Ohm's Law. Fig. 6 shows that the overall voltage difference on the power plane of the Z-EBG is 133.388 mV. So the dc resistance is 133.388 mQ. By contrast, the dc resistance increases 12.337 mQ because of the inserted DBCSRR structure.



Figure 5. Voltage distribution for the proposed structure. (a) Power plane. (b) Ground plane.



Figure 6. Voltage distribution for the traditional Z-EBG structure.

4. SIGNAL INTEGRITY OF THE PROPOSED STRUCTURE

Although the novel proposed EBG structure can effectively suppress the SSN, it leads to deterioration of signal transmission quality because the power plane is not complete. Therefore, it is necessary to analyze the signal transmission characteristics. As shown in Fig. 7(a), we consider a transmission line with a length of 60 mm. The trace propagates from the first layer to the forth layer and back to the first layer with two via transitions along the path. The second and third layers of the PCB are the patterned power plane and the solid ground plane, respectively. The space between adjacent layers is 0.4 mm. Besides, Port 1 and Port 2 are located at both ends of the signal line. The side view of PCB is shown in Fig. 7(b).

A nonreturn to zero (NRZ), pseudo random binary sequence (PRBs) is sent at Port 1, and the signal propagation characteristics are monitored at Port 2. The transmitted PRBs is coded at 2.5 Gb/s, with 0.5 V swing, and the nominal rise/fall time is 125 ps. The eye patterns of the L-EBG plane and the proposed structure are generated by using Ansoft Designer V4, as shown in Fig. 8. It is seen that

Table 1. Comparison result of the L-EBG and the proposed EBG structure.

	MEO	MEW
L-EBG	$225\mathrm{mv}$	$350\mathrm{ps}$
Proposed EBG	$250\mathrm{mv}$	$420\mathrm{ps}$

MEO = 225 mV, and MEM = 350 ps for L-EBG, and MEO = 250 mV and MEW = 420 ps for the proposed EBG board respectively, as shown in Table 1.



Figure 7. Four-layer structure with single-ended trace transmit between the patterned power plane and solid ground plane. (a) The three-dimensional view. (b) The side view.



Figure 8. Simulated eye patterns for (a) the L-EBG board and (b) the proposed EBG board.

5. CONCLUSION

To conclude, a novel Z-EBG structure embedded by a DBCSRR filter is proposed in this paper to enhance the ability of SSN suppression. Compared with the traditional L-bridge EBG structure, the novel structure has a great improvement on the width of stopband and suppression depth. The stopband ranges from 270 MHz to more than 20 GHz at the restraining depth of -30 dB. Meanwhile, we use equivalent circuit models to analyze lower cutoff and upper frequency of the proposed structure, respectively. Then analysis of IR-Drop and DC resistance is obtained by CST simulation. Finally, the effect of the new structure on SI is investigated. And the structure is met with signal integrity by time-domain simulation results in high-speed circuit.

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